

1

## **ABSTRACT OF THE DISCLOSURE**

2 A semiconductor integrated circuit device is disclosed that can provide greater  
3 flexibility of layout while essentially ensuring circuit characteristics, and at the same time  
4 providing an minimum electrostatic discharge breakdown withstand value according to  
5 Charged Device Model (CDM) at all input/output (I/O) terminals. For each I/O terminal a  
6 size of a CDM protective device can be optimized in response to reference electric potential  
7 wiring resistance between an input protective device, a MOSFETs that can constitute an  
8 internal circuit, and an input resistance.